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Over the past two decades, technologies for microsystems fabrication have made considerable progress. This has made possible a large variety of new commercial

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Devices ranging, for example, from integrated pressure and acceleration microsensors to active micromirror arrays for image projection. In the near future, there will be a number of new devices, which will be commercialized in many application areas. The field of microsystems is characterized by its wide



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diversity, which requires a multidisciplinary approach for design and processes as well as in application areas. Although there is a common technological background derived from integrated circuits, it is clear that microsystems will require additional application-specific technologies. Since most microsystem

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Technologies are based on batch processing and dedicated to mass production, prototyping is likely to be an expensive and time-consuming step. It is recognized that standardization of the processes as well as of the design tools will definitely help reduce the entry cost of microsystems. This creates a very

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Challenging situation for the design, modeling and simulation of microsystems. Methodology for the Modeling and Simulation of Microsystems is the first book to give an overview of the problems associated with modeling and simulation of microsystems. It introduces a new methodology, which is supported by

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Several examples. It should provide a useful starting point for both scientists and engineers seeking background information for efficient design of microsystems.

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Provides an overview of embedded software development with the emphasis on low-level I/O access and drivers. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals, including PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD

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(secure digital) card. Part IV provides three case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. The book utilizes FPGA devices, Nios II

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soft-core processor, and development platform from Altera Co., which is one of the two main FPGA manufactures. Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions (details at <http://www.altera.com/university> ). The

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Two main educational prototyping boards are known as DE1 (\$99) and DE2 (\$269). All experiments can be implemented and tested with these boards. A board combined with this book becomes a “turn–key” solution for the SoPC design experiments and projects. Most HDL and C codes in the book are device



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binaryToESeg Driver 4 Creating Ports For  
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